



PATENT

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of

Hideki IWAKI et al.

Serial No. 09/994,683

Filed: 28 November 2001

MULTI-LEVEL CIRCUIT SUBSTRATE, : For:

METHOD FOR MANUFACTURING SAME AND METHOD FOR ADJUSTING:

CHARACTERISTIC IMPEDANCE

THEREFOR

Art Unit:

2812

Examiner:

Leonardo ANDU

Attorney Docket:

P64916US1

RESPONSE TO RESTRICTION REQUIREMENT

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

Applicant in response to the Office Action dated 26 February 2003 (Paper No. 4), hereby elects Claim 1 through 11 for further prosecution in this application.

An early and favorable action is respectfully solicited.

Respectfully submitted,

JACOBSON HOLMAN, PLLC

Date: 24 March 2003

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P64916US1 Attorney Docket:

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